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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Seiji Funaba, et al.

**Examiner:** Benjamin P. Sandvik

**Serial No:** 10/672,551

**Art Unit:** 2826

**Filed:** September 26, 2003

**Docket:** 17072

**For:** SEMICONDUCTOR UNIT  
HAVING TWO DEVICE  
TERMINALS FOR EVERY  
ONE INPUT/OUTPUT SIGNAL

**Dated:** October 19, 2005

**Conf. No.:** 3724

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

Pursuant to the Restriction Requirement imposed in the Official Action dated September 20, 2005, Applicants elect the first embodiment disclosed in Figures 4-7, 16, 18, 23, 26 and 27 for continued prosecution herein. Claims 1, 2, 5, 7, 11, 13, 14, 17, 19, 23, 25, 31, 35, 55, 60, 61, 68 and 72 currently read on the first embodiment disclosed in Figures 4-7, 16, 18, 23, 26 and 27.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 19, 2005.

Dated: October 19, 2005

  
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Paul J. Esatto, Jr.